

## RECEIVED (-02.04

JUN 0 7 2004

## OFFICE OF PETITIONS

Joc4700

AAR

Att 18 W

Approved for use through 07/31/2006, OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Application Number

(				Application Number	09/87	°138	<b>'3</b>	
TRANSMITTAL FORM  (to be used for all correspondence after initial filing)				Filing Date	05.31.2001 Edlin Solomon			
				First Named Inventor				
			filing)	Art Unit	2815 Nguyen Joseph			
				Examiner Name				
Total Number of Pages in This Submission			52	Attorney Docket Number	0_0			
ENCLOSURES (Check all that apply)								
F:	Fee Transmittal Form  Fee Attached  Amendment/Reply			Drawing(s)  Licensing-related Papers  Petition  Petition to Convert to a	After Allowance communication to Group  Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)  Proprietary Information  Status Letter Other Enclosure(s) (please			
After Final Affidavits/declaration(s)				Provisional Application Power of Attorney, Revocation Change of Correspondence Addre				
Extension of Time Request				Terminal Disclaimer		ntify belo		
Express Abandonment Request				Request for Refund				
Information Disclosure Statement				CD, Number of CD(s)				
Certified Copy of Priority Document(s)			Rema	rks				
Response to Missing Parts/ Incomplete Application								
Response to Missing Parts under 37 CFR 1.52 or 1.53								
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT								
Firm or Individual name		Edlin	Sol	omon				
Signature		2gum						
Date 05.28.2004								
CERTIFICATE OF TRANSMISSION/MAILING								
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.								
Typed or printed name  Edlin Solomon  Signature  Date 05. 28.2004								
Signature 3a.		3am		•		Date	05.28.2004)	

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.



Examiner -- Joseph Nguyen Applicant -- Edlin Solomon Reply mailed 05.28.2004. Application 09/871,383(20020089000-A1). Art unit 2815.Mailing date 9.27.2002.

Reply No.9 to Notice received on 10.18.2002.

H01L 29/06 H01L 29/70

## BIDIRECTIONAL BIPOLAR STATIC INDUCTION DEVICE

"paragraph 0006". This result is achieved by disposing elements of the bipolar static induction transistors: [[two]] a gate, [[four]] sources and channels and six electrodes on either side of a lightly doped n-type silicon monocrystal substrate, and besides one of said channels channel of the multielemental structure is thicker than the other normally-off channels on either side of said substrate and said of said substrate and said of said substrate.

"between paragraph 0006 and 0007". This result is achieved by a layer of a doped n+-type polysilicon is disposed on the silicon monocrystal surface.

This result is achieved by the control over both hole emission into and extraction out the lightly doped area are used as well as the current feedback for said control over emission into one.

This result is achieved by the thickness of said channels are small and the impurity concentration near said gates is high enough.

"paragraph 0007". This result is achieved by disposing an epitaxial <u>layers</u> <u>layer</u> of the same type of conductivity with the impurity concentration about 10.sup.17 cm.sup-3 on either side of said substrate; said gate, said sources and said channels are disposed in said epitaxil <u>layers</u> <u>layer</u>.

"between paragraph 0007 and 0008". This result is achieved by a layer of a doped n+-type polysilicon is disposed on the silicon monocrystal surface.

This result is achieved by said thick <u>channels are</u> channel is normally-on [[ones]] one.

"paragraph 0008. The offered transistors and transistor-thyristors can be applied for production, transfer and use of electric energy within a very broad range of power: from the control of electrical soldering to the control of most powerful turbogenerators and thermonuclear stations. They are effective for designing electronic transformers, power supply units, and "flexible transfers of alternating current". In the latter case <u>transistors</u> transistor-thyristors can be connected in series, which will allow to easily create high voltage system with operating voltage 10.sup.6 V and more with a control with light signals or by wireless. These transistors can be most widely used in the devices aimed at defending people from electric shock. They can also be used in systems with the unipolar power supply transmitting energy in both directions -- both from a source to a load (resonator) and from the load to the source. It will make it possible to